

FLAP DRIVER D722 NOTES

U6, PIN 4 IS CAMERA SAFE=0 BEFORE TIME DELAY WHEN:
DAY; FLAP CLOSED
NIGHT; FLAP CLOSED
NIGHT; FLAP COMPLETELY OPEN

U6, PIN 4 IS UNSAFE=1 WHEN:
DAY; FLAP COMPLETELY OPEN
DAY OR NIGHT; FLAP PARTIALLY OPEN
DAY OR NIGHT; FLAP CLOSE AND FLAP OPEN SWITCHES ARE
SIMULTANEOUSLY ARE "0"

MOTOR

MOTOR CURRENT IS APPROXIMATELY 25MA WHILE FLAP IS IN MOTION
MOTOR STALL CURRENT IS 1.1A AT 12.5V
MOTOR STALL CURRENT LIMITED TO 100MA ON FC BOARD AND TURNS
MOTOR POWER OFF UNTIL MANUALLY RESET WITH PUSHBUTTON.
CHECK FLAP FOR OBSTRUCTION BEFORE RESETTING OR DAMAGE
TO GEAR TRAIN MAY RESULT

320MS DELAY

WHEN CHANGING DIRECTION WHILE IN MOTION A 100MS CURRENT SPIKE
OCCURS. DELAY ADDED SO OVER CURRENT DETECTOR WILL NOT SHUT
MOTOR OFF

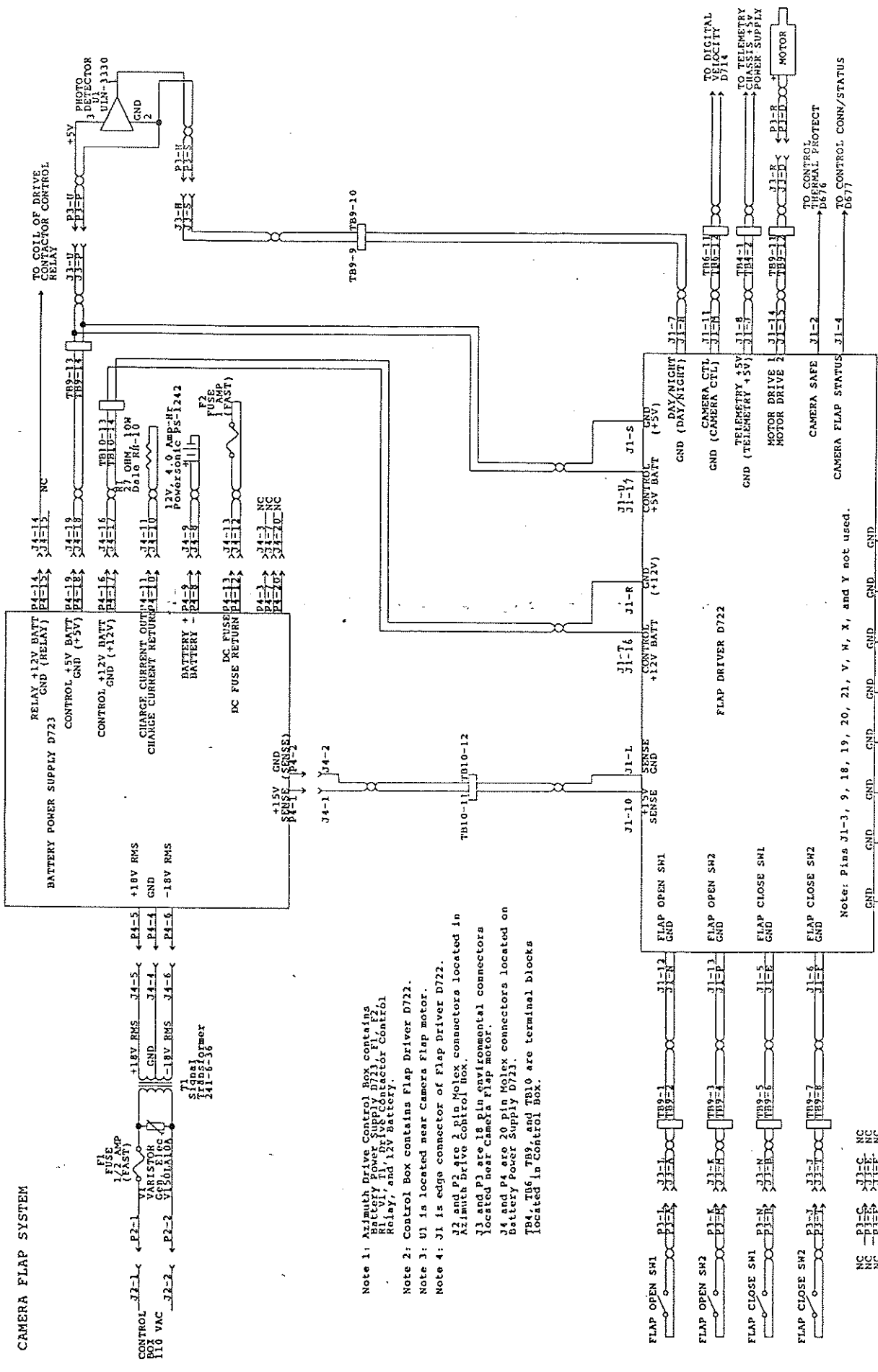
ANTENNA CANNOT MOVE

UNSAFE

FLAP DRIVER BOARD NOT INSTALLED

12V BATT. GND INSTALLED AT J1-R. THE DRIVE CONTACTOR
CONTROL RELAY RETURNS TO CONTROL BOX PWR SUPPLY COMMON
COMES ONTO THIS BOARD ON J1-1, J1-22, J1-A, J1-Z. THE
12V BATT. GND. AND PWR SUPPLY COMMON ARE CONNECTED
THROUGH BOARD. JUMPER TB10-14 TO PWR SUPPLY COMMON TO
ENABLE ANTENNA MOVEMENT.

CAMERA FLAP SYSTEM



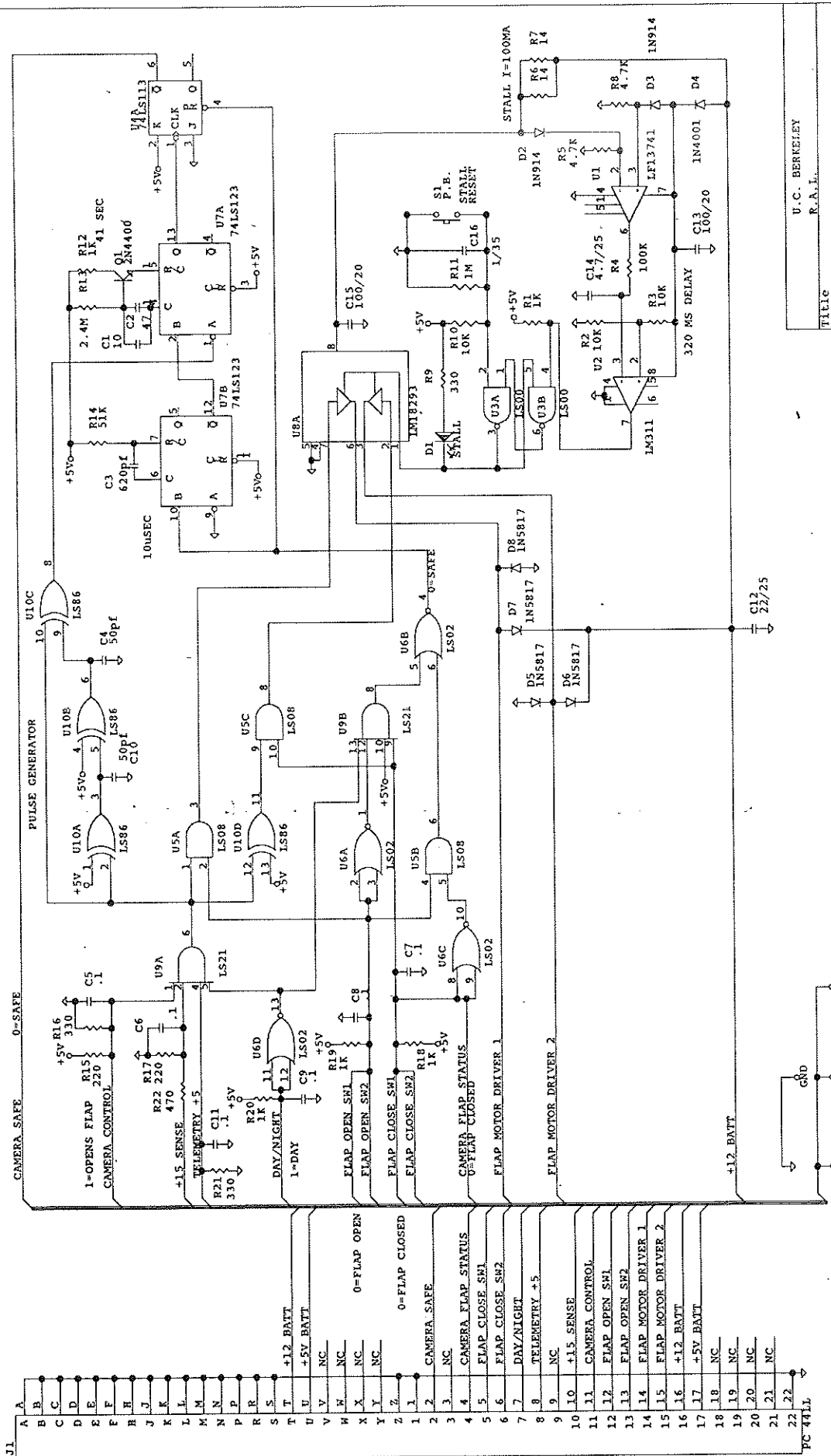
- Note 1: Azimuth Drive Control Box contains Rectifier Tube, Variable Capacitor Control Relay, and 12V Battery.
 - Note 2: Control Box contains Flap Driver D722.
 - Note 3: U1 is located near Camera Flap motor.
 - Note 4: J1 is edge connector of Flap Driver D722.
- J2 and J3 are 2 pin Molex connectors located in Azimuth Drive control box.
 J3 and J4 are 18 pin environmental connectors located near Camera Flap motor.
 J4 and P4 are 20 pin Molex connectors located on Battery Power Supply D723.
 TB4, TB8, TB9, and TB10 are terminal blocks located in Control Box.

Note: Pins J1-3, 9, 18, 19, 20, 21, V, M, X, and Y not used.

J1-1	J1-22	J1-A	J1-B	J1-C	J1-D	J1-K	J1-Z
J1-1	J1-2	J1-3	J1-4	J1-5	J1-6	J1-7	J1-8
J1-9	J1-10	J1-11	J1-12	J1-13	J1-14	J1-15	J1-16
J1-17	J1-18	J1-19	J1-20	J1-21	J1-22	J1-23	J1-24
J1-25	J1-26	J1-27	J1-28	J1-29	J1-30	J1-31	J1-32

NC	J1-C	J1-E	J1-F
NC	J1-G	J1-H	J1-I
NC	J1-J	J1-L	J1-M
NC	J1-N	J1-O	J1-P

FLAP DRIVER D722

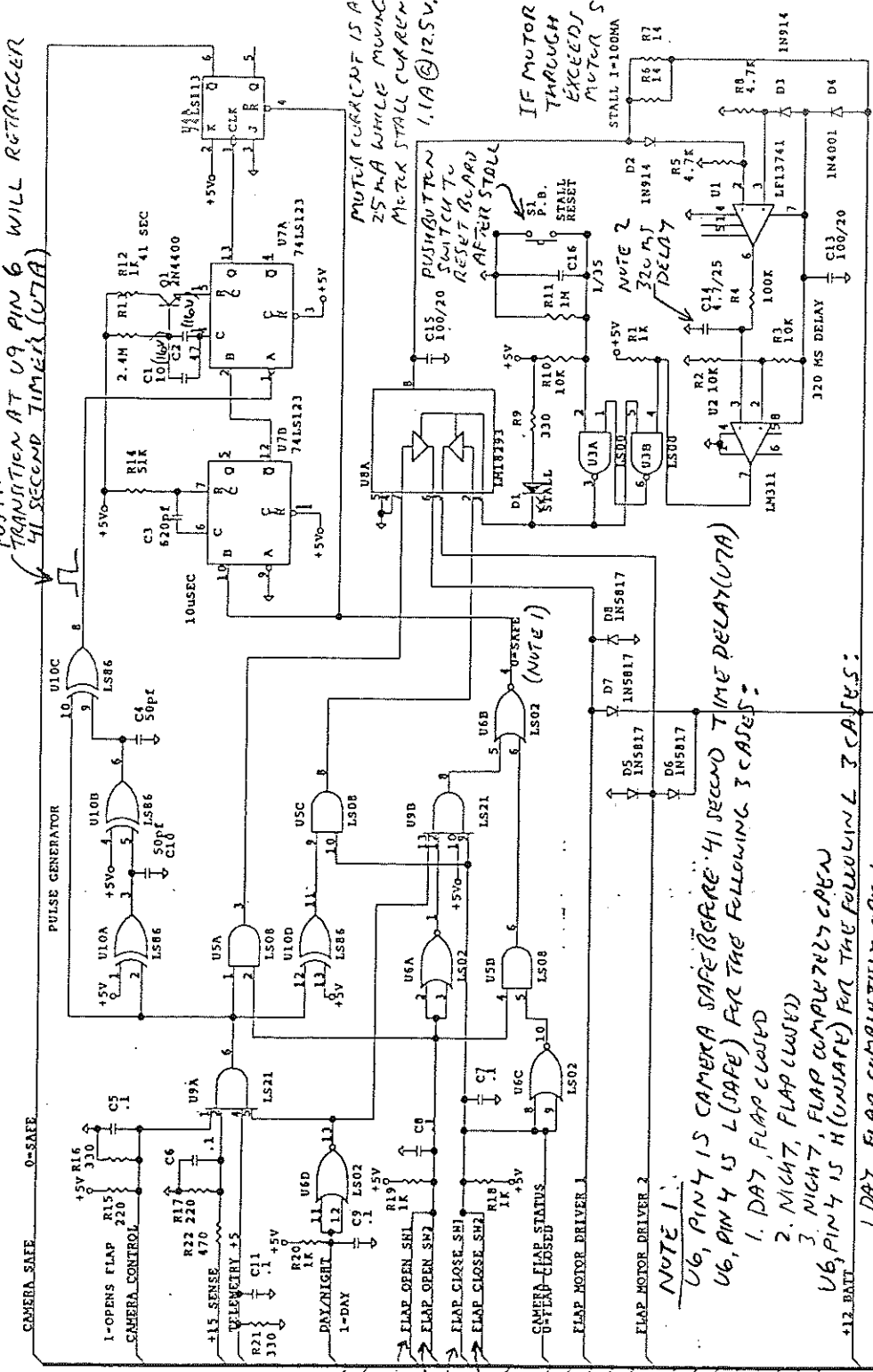


U.C. BERKELEY
R.A.L.L.
FLAP DRIVER
Document Number
D722
REV
DATE: APR 13, 1972 SHEET 1 OF 1

FLAP DRIVER D722

NOTE 2: CHANGING DIRECTION OF MOTOR RESULTS IN A LARGE 100MS MOTOR CURRENT SPIKE. ADD R4, C14 TIME DELAY SO THAT MOTOR STALL DETECT CIRCUIT WILL NOT TRIP DURING SPIKE.

POSITIVE PULSE GENERATED ON EVERY TRANSITION AT U9, PIN 6 WILL RETRIGGER 41 SECOND TIMER (U7A)



MOTOR CURRENT IS APPROX. 25mA WHILE MOVING FLAP. MOTOR STALL CURRENT IS 1.1A @ 12.5V.

IF MOTOR CURRENT EXCEEDS 100MA THROUGH R6 AND R7 MOTOR STALL DETECT CIRCUIT WILL TURN OFF POWER TO MOTOR AND TURN ON LED.

NOTE 1: U6, PIN 4 IS CAMERA SAFE (BEFORE 41 SECOND TIME DELAY (U7A)) U6, PIN 4 IS L(SAFE) FOR THE FOLLOWING 3 CASES:
 1. DAY, FLAP CLOSED
 2. NIGHT, FLAP CLOSED
 3. NIGHT, FLAP COMPLETELY OPEN
 U6, PIN 4 IS H (UNSAFE) FOR THE FOLLOWING 3 CASES:
 1. DAY, FLAP COMPLETELY OPEN
 2. DAY OR NIGHT, FLAP PARTIALLY OPEN
 3. DAY OR NIGHT, FLAP OPEN LIMIT SWITCH AND FLAP OPEN LIMIT SWITCH ARE SIMULTANEOUSLY CONNECTED TO GROUND. (DEFECTIVE LIMIT SWITCH)

U.C. BERKELEY
R.A.L.L.
FLAP DRIVER
Size/Document Number
A D722
DATE: APR 15, 1972
REV

A	+
B	+
C	+
D	+
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